Agenda for next couple weeks

* Order boards (3) as soon as possible
* Robby needs to do board 2
* Get comms setup working
* Get UART working
* Submit to competition
* Solder on components to board
* Start prototype v3

This meeting is a design review

Changes to power

* Added test points in locations
* Add current sensing resistors
* Fixed GPOUT and BIN to no longer be floating
* Test points on top
* TO DO: Silkscreening (labels)
* Weird things: BGA (fuel gauge), needed to fudge the pads footprint by 2 mil to drop via in
* GW: what happens if solder bridges?
* BT: failsafe: if fuel gauge isn’t working, we can resolder connections in fuel gauge bc Brady put in a 0 - ohm resistor which we could disconnect. Allow power to battery to charger, but
* Failure mode: short at A2 SDA and GND- would render entire bus (to temp sensor) useless
* MacroFab has an extended DRC, but it is more expensive, takes more time to fab
* No option for non-BGA component, that’s the only size it comes in
* GW: could you call them and see what MF have to say? And also ask Erik, he already has more experience with this so he might have an opinion about risk?
* GW: do we have way bypass solar sensor to put in known amount of current?
  + BT: yes, put power thru testpoint and GND testpoint
* GW: other
* BT: a couple DNP (do not place) on clocking (in case need 10k resistors to pullup)
* 0-ohm resistor to buck-boost in case we need to take that part out
* GW: if you want to inject 5V, then desolder 0-ohm resistor and use jumper from pin 5 to gnd
* JH: could power dust sensor separately anyways if 5V from buck boost isnit working
* GW: do you have way to disable Energy Harvester?
* RN: if you’re willing to GND the buck-boost, you could always do the same but to 3.3V
* GW: for EH, you can disconnect solar cell, feed in known source, and measure output
* If fuel gauge is DOA, disconnect 0-ohm resistor, but might still be dead short to GND bc of via adjustment. Then what? If c2 shorted to gnd, then EH wont work. What if you put a DNP or test points across a 0-ohm resistor out of c2 to test EH and ?
* Need testpoint @ Vbatt

Comms Board

* Added footprints for ext osc
* Added leds for testing
* Shotkey diodes
* Bypass resistor to skip transistor (for being able to turn on/off power to comms)
* Need to switch diode to pin 1, from GND to power
* Trace size need to use to 17 mils
* Clearance issue at pins
* If issue with the antenna, have option to disconnect and 0-ohm resistor to the SMA
* GW: put more vias to get routing away from the IC--- that’s to wick away heat mostly not signal